

FIG. 1A

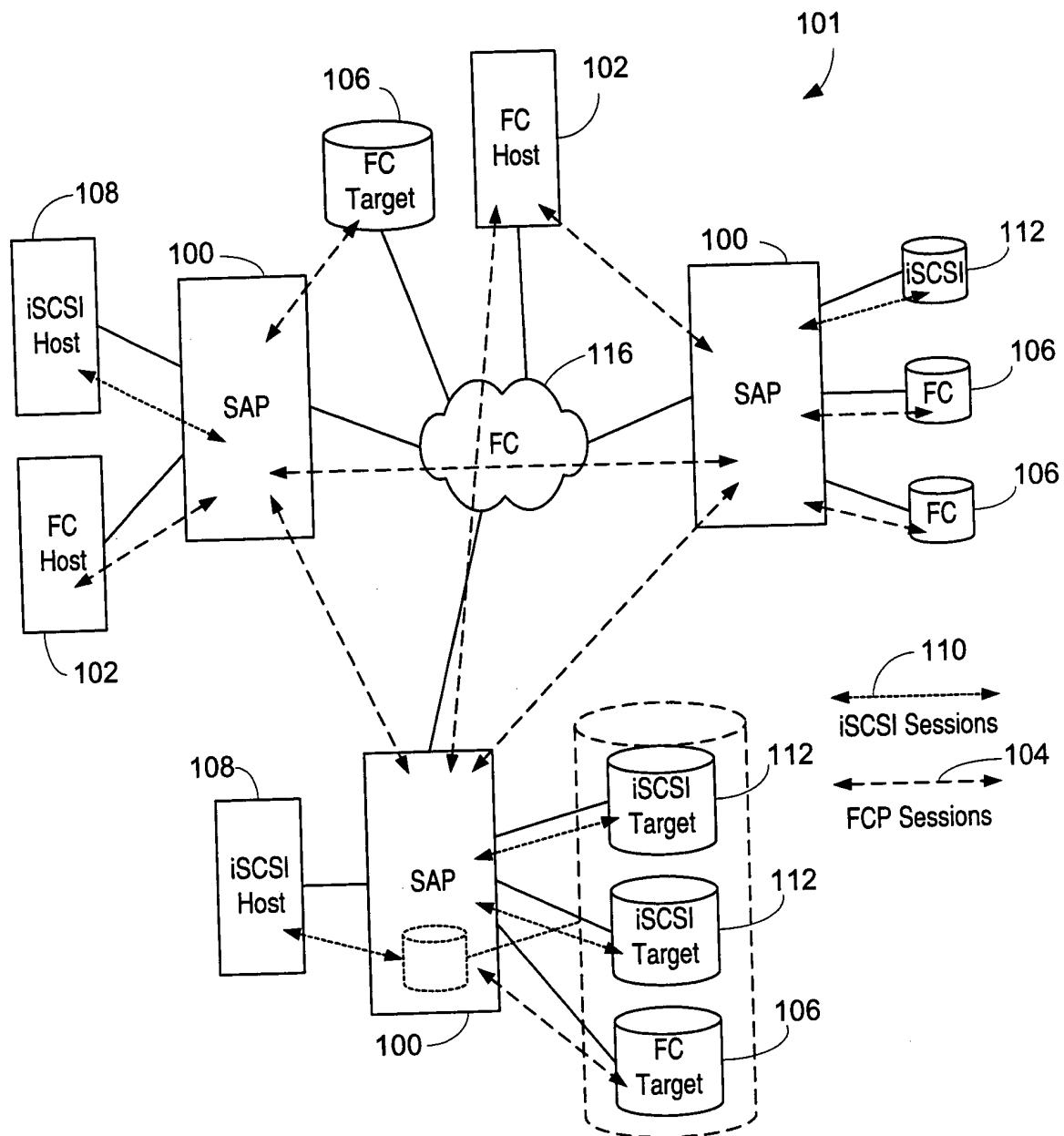


FIG. 1B

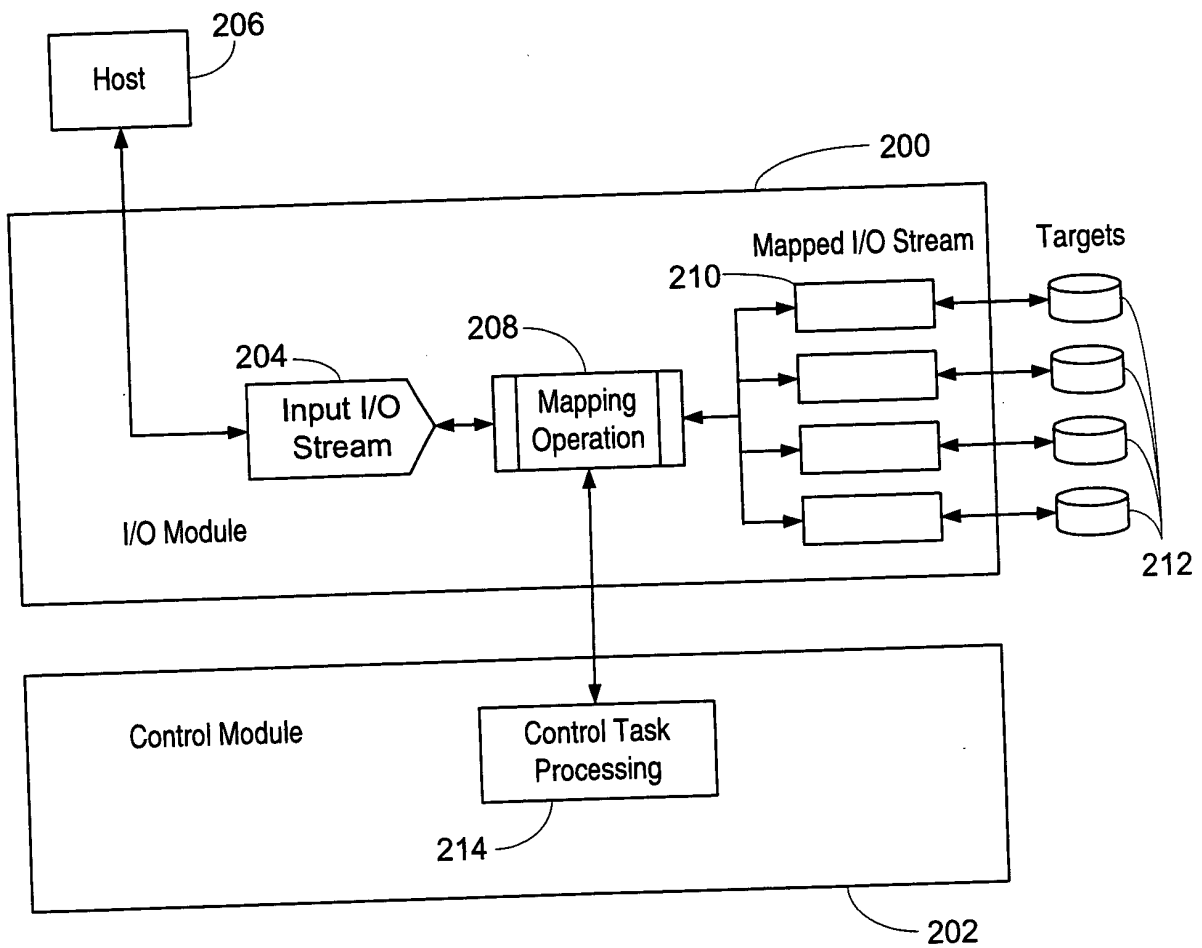


FIG. 2

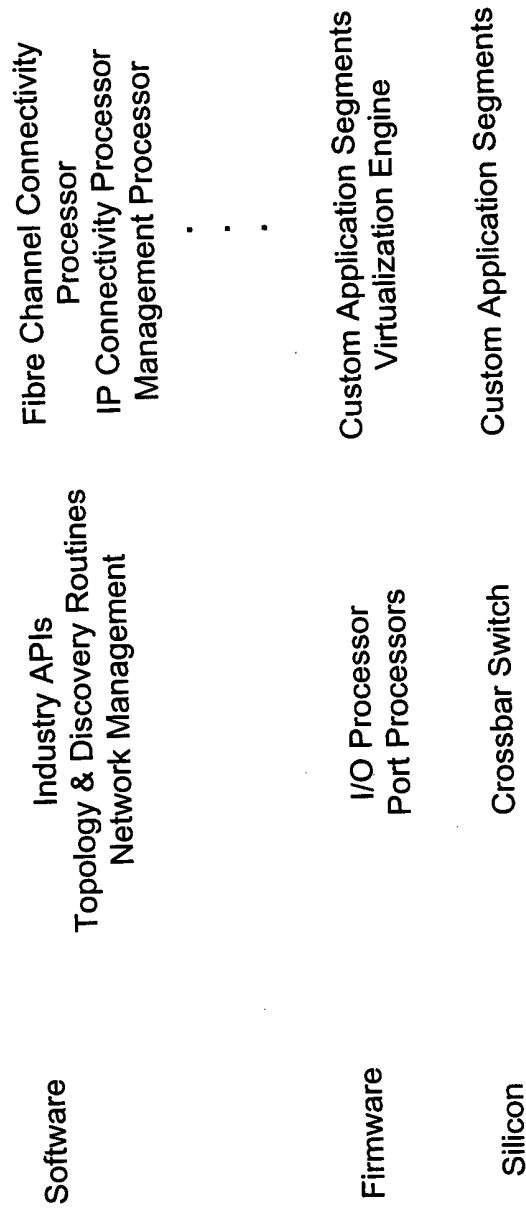


FIG. 3

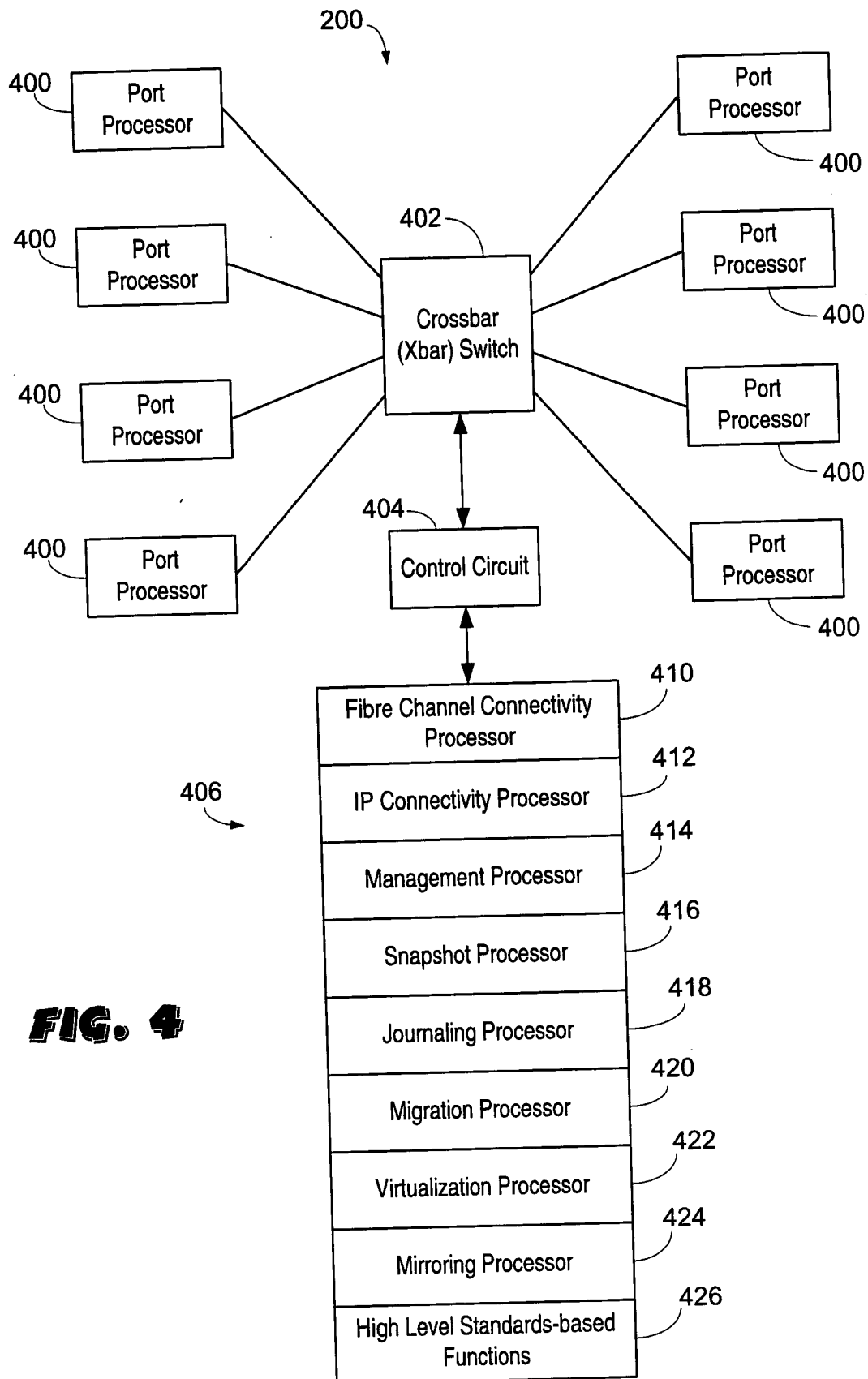


FIG. 4

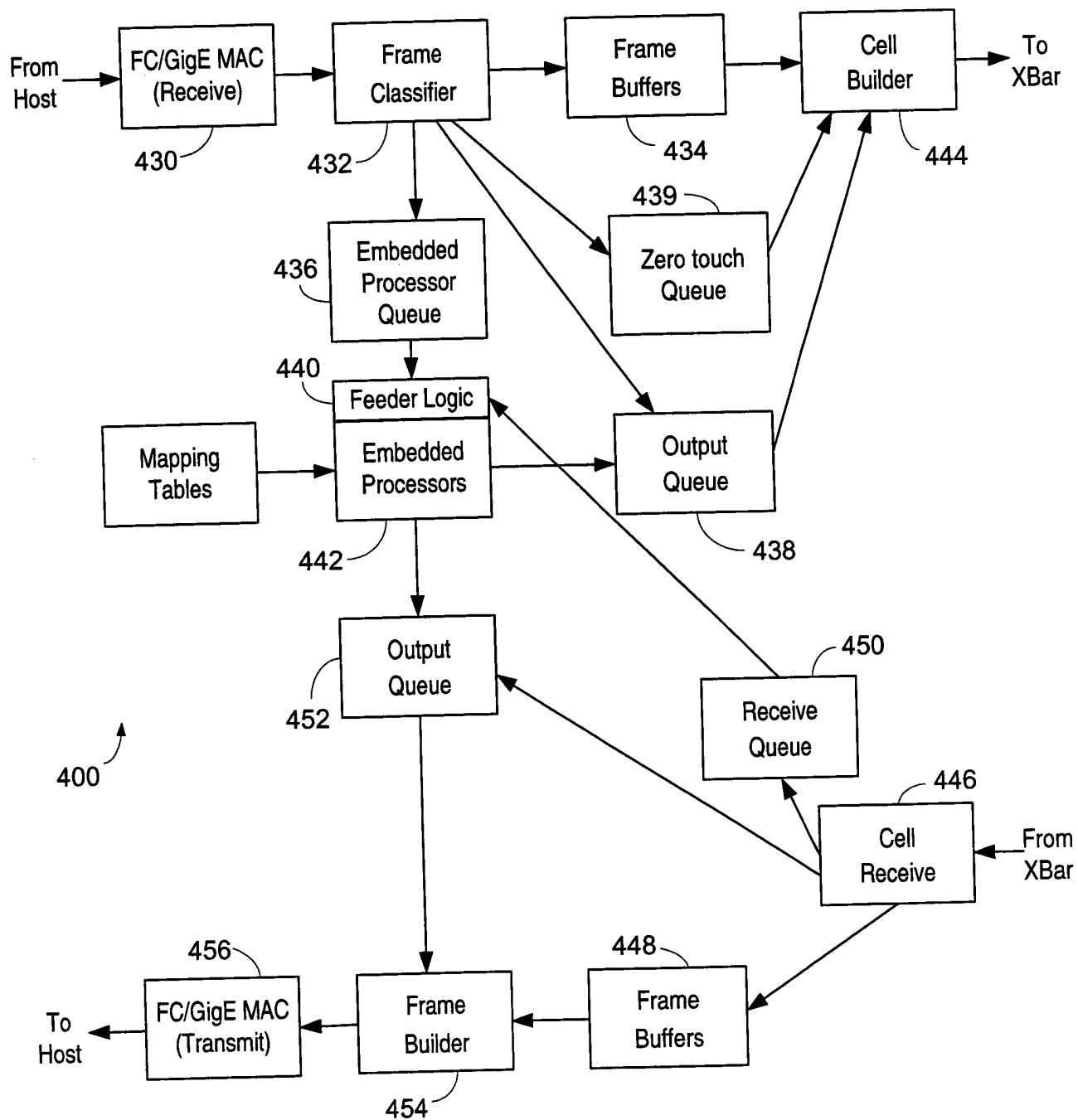


FIG. 5

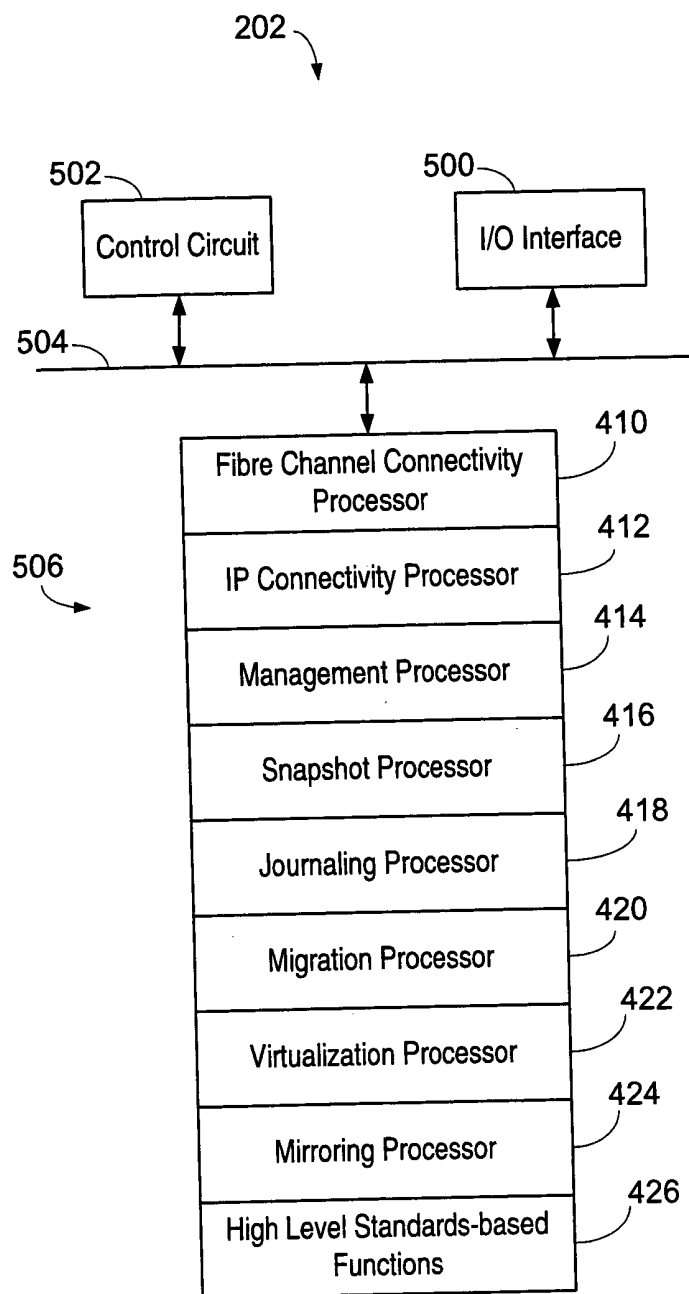


FIG. 6

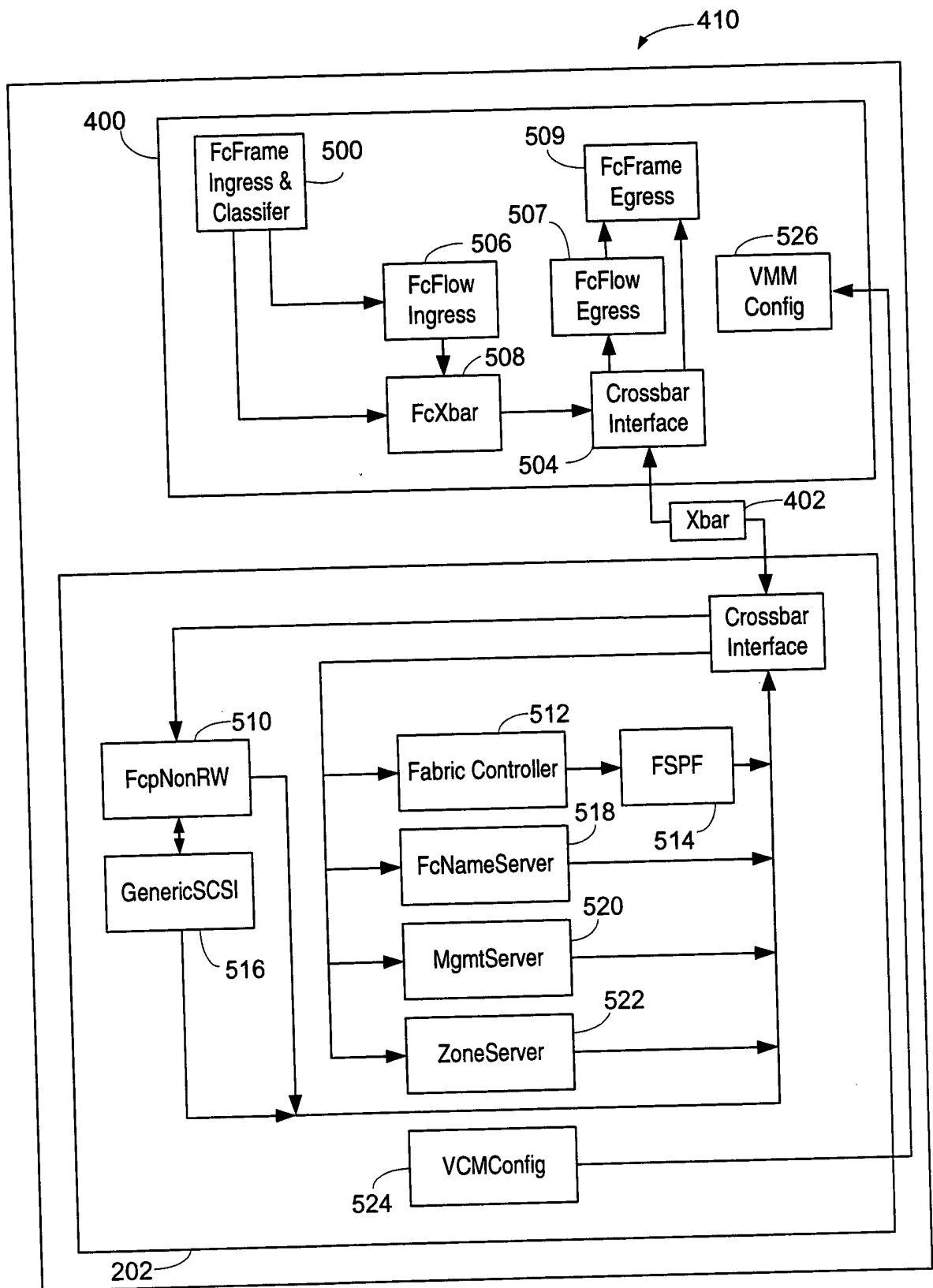


FIG. 7

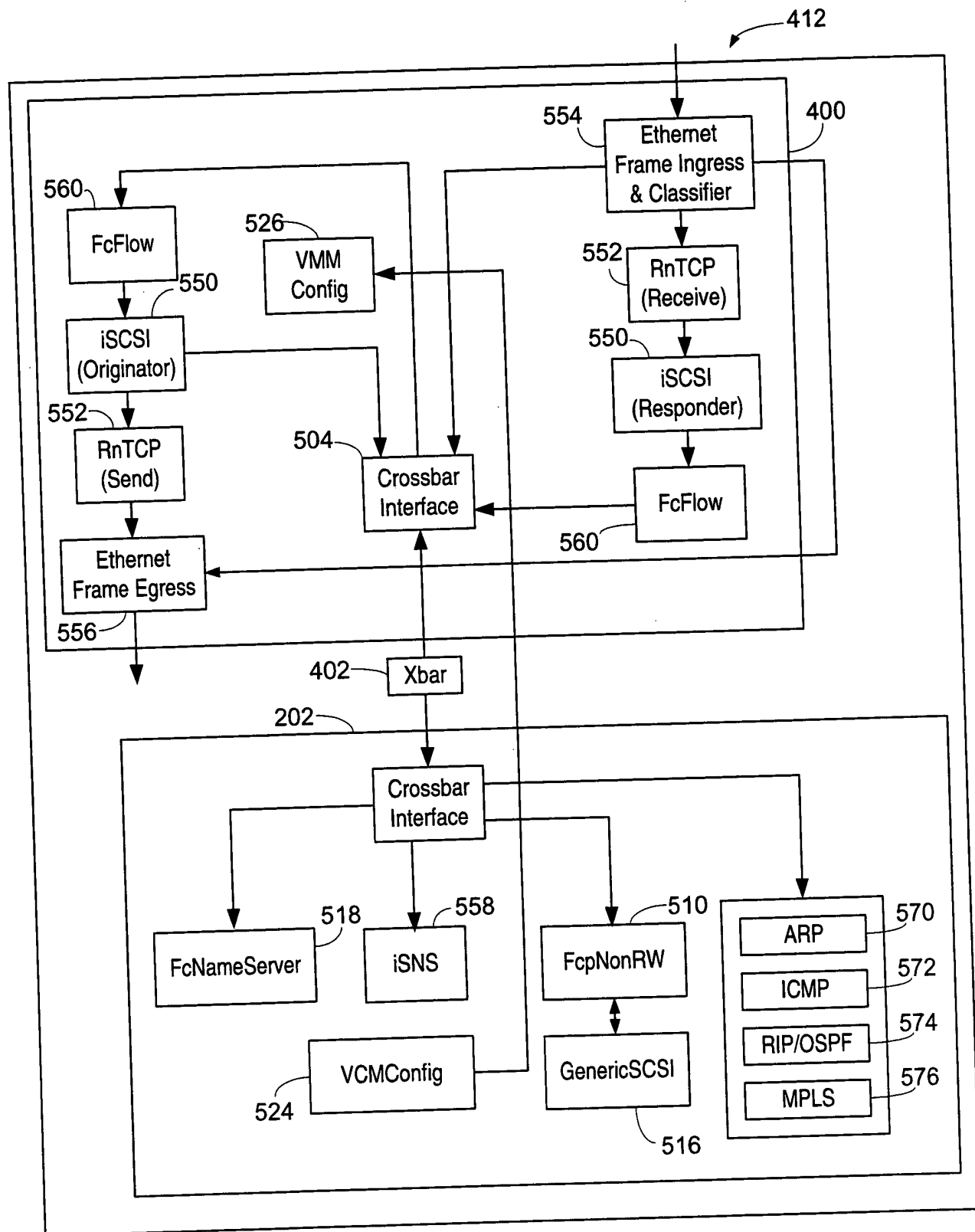


FIG. 8

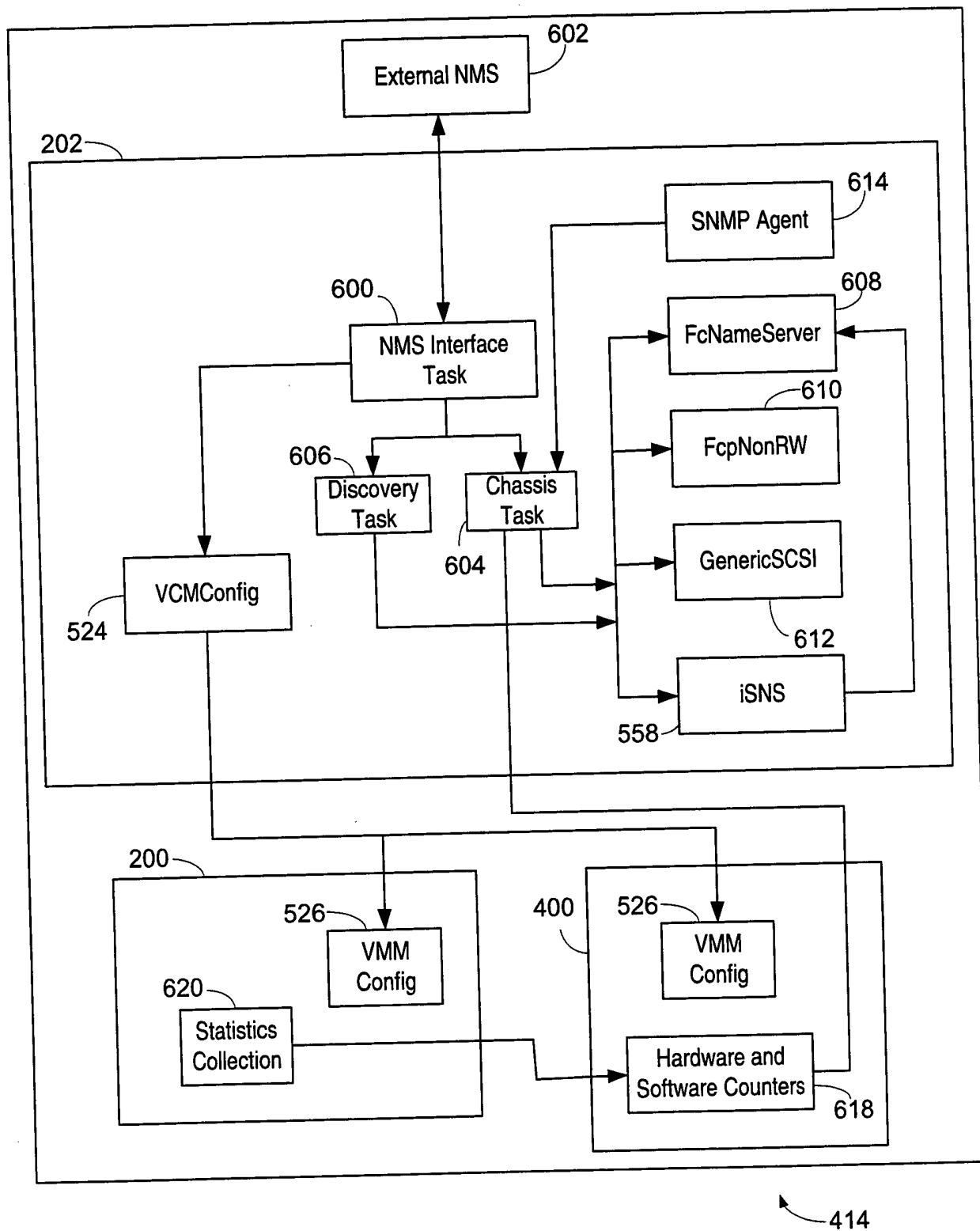


FIG. 9

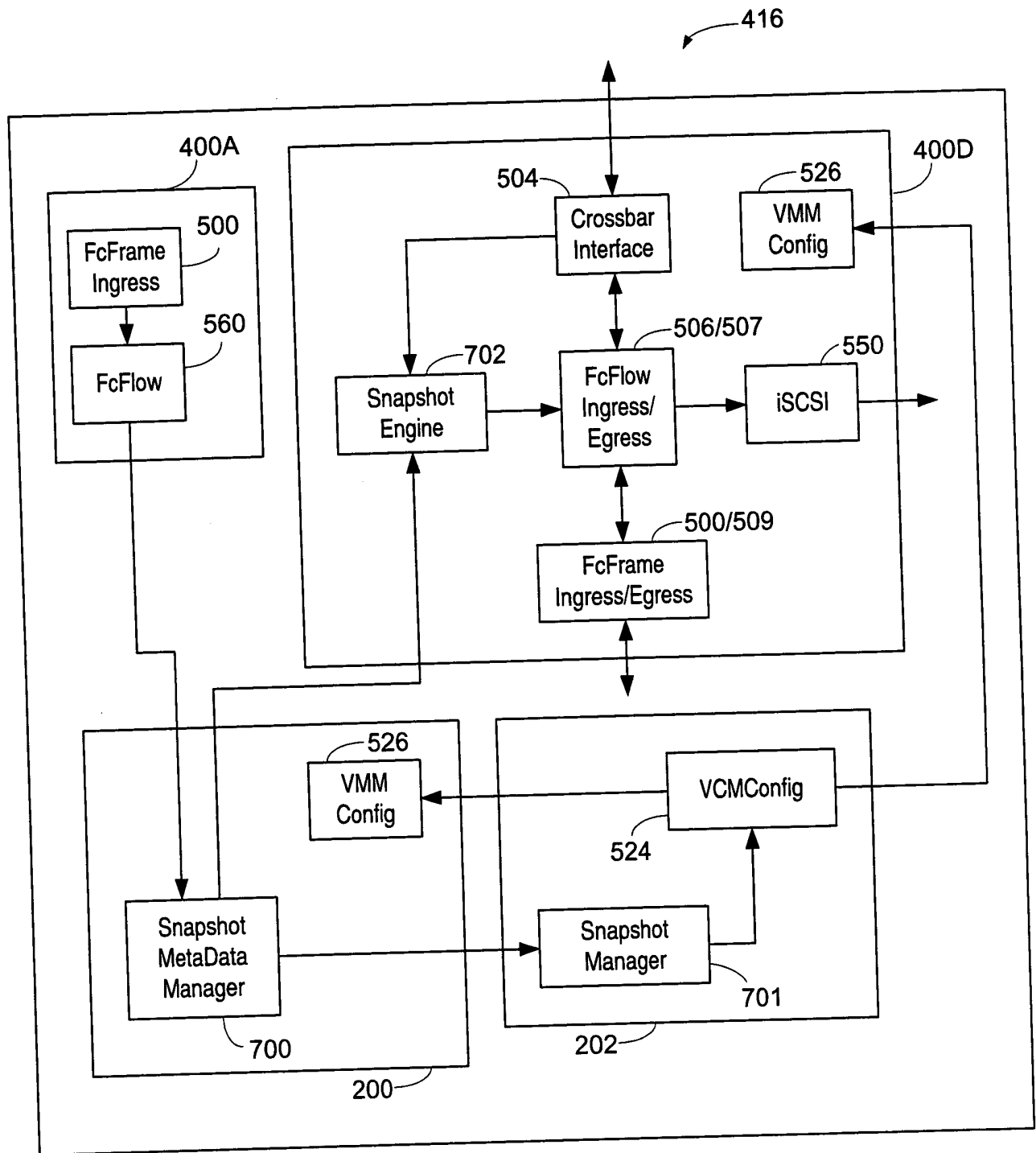


FIG. 10

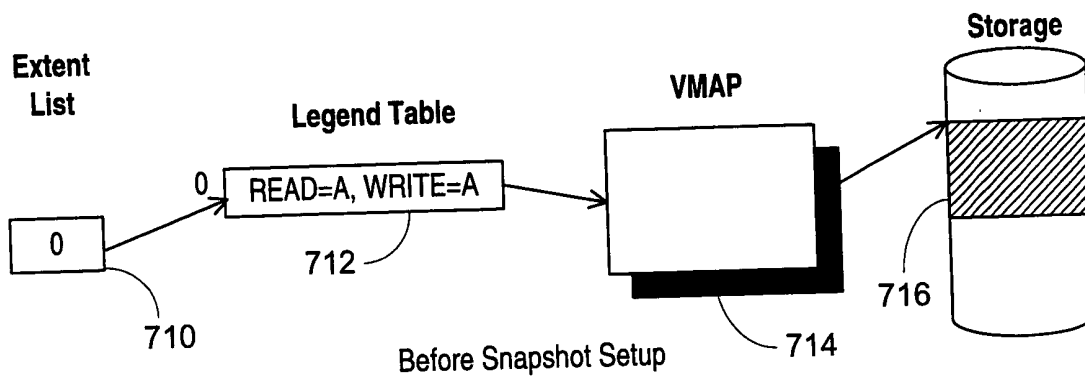


FIG. 11

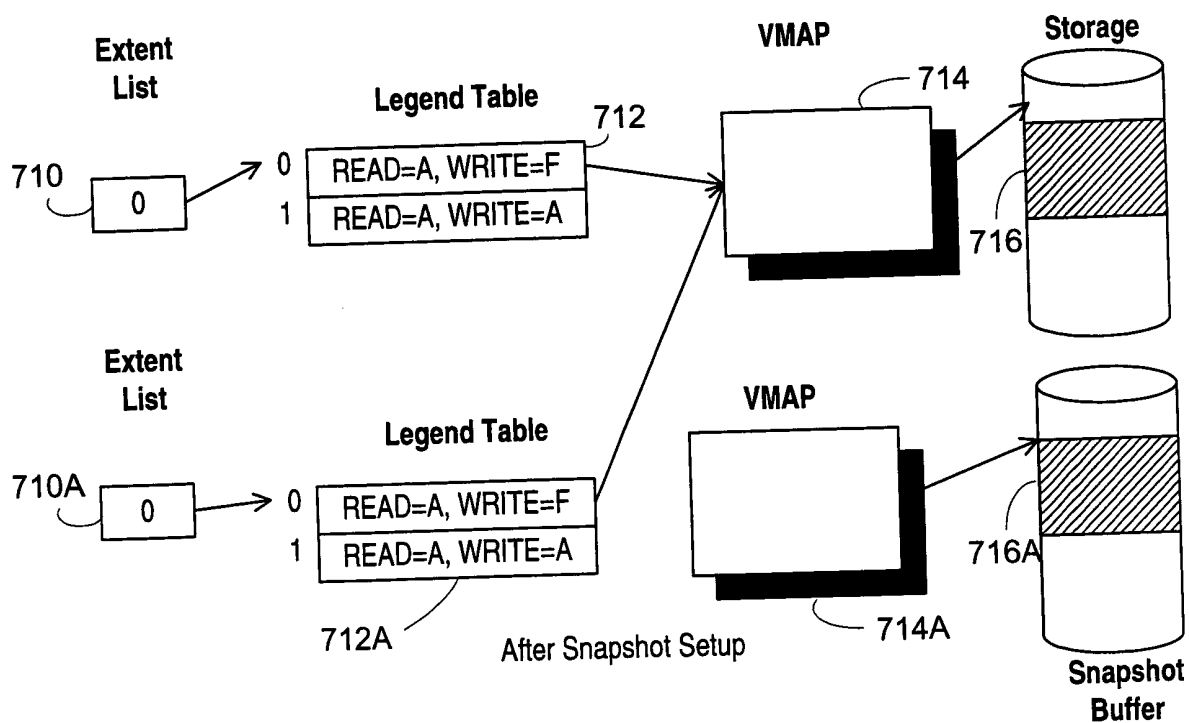


FIG. 12

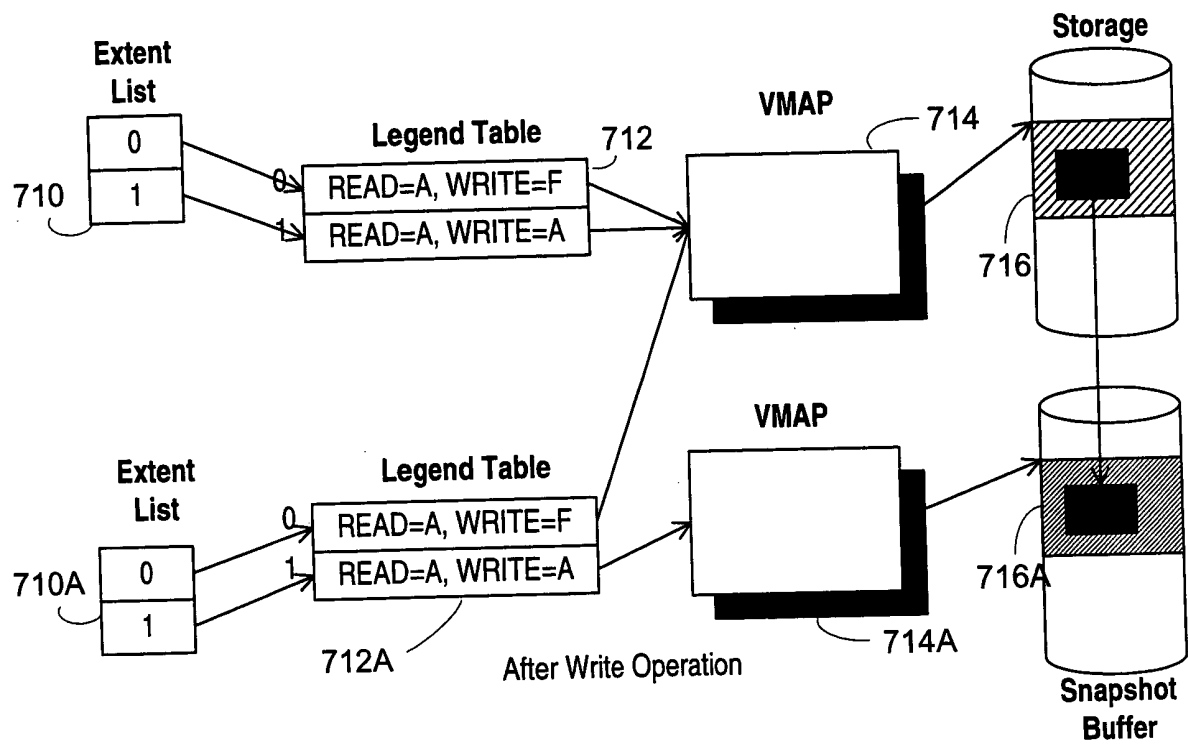


FIG. 13

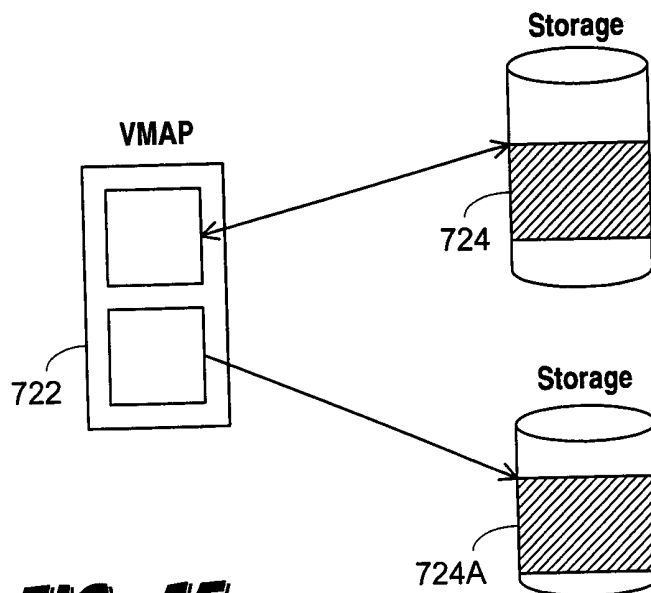


FIG. 15

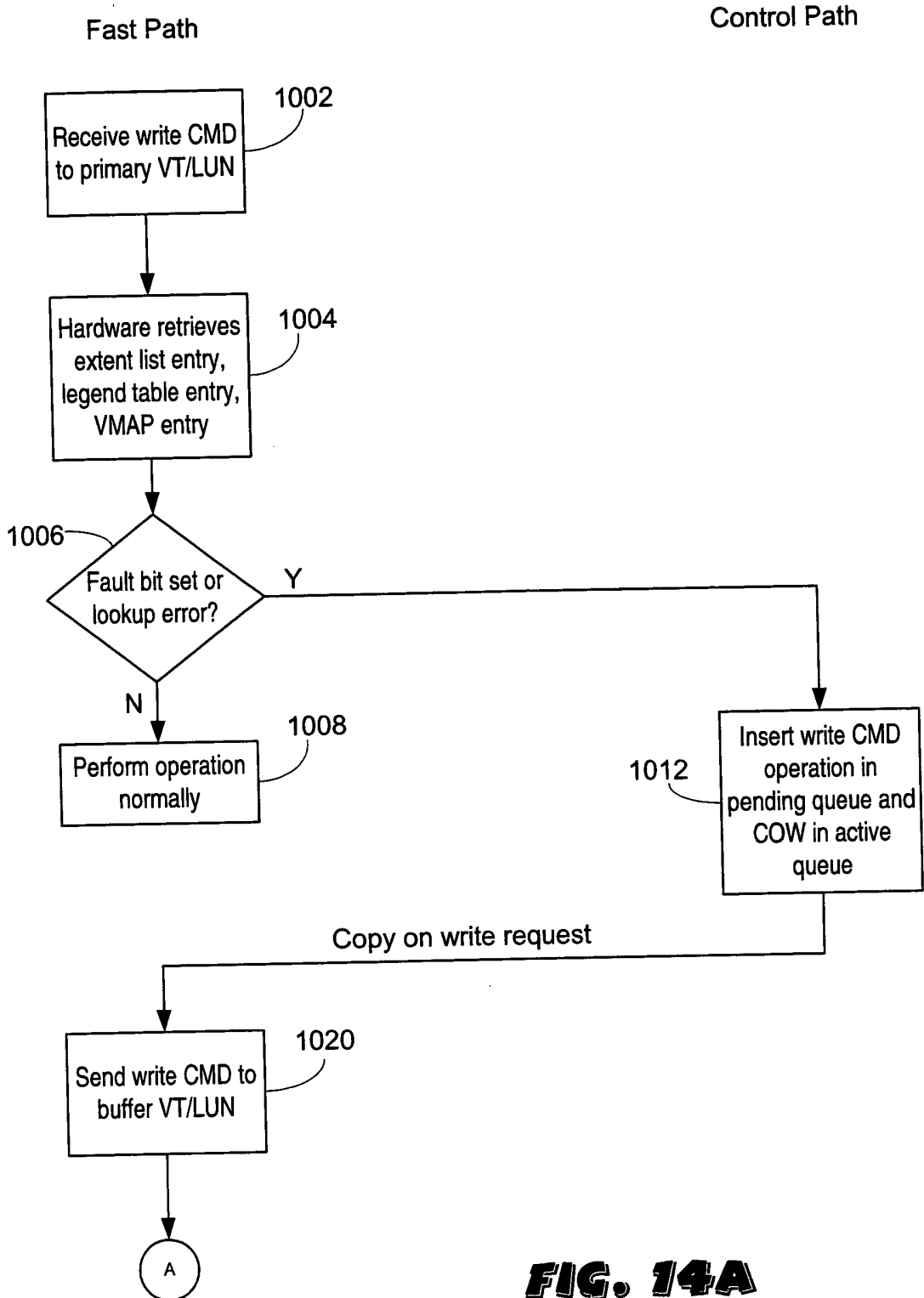


FIG. 14A

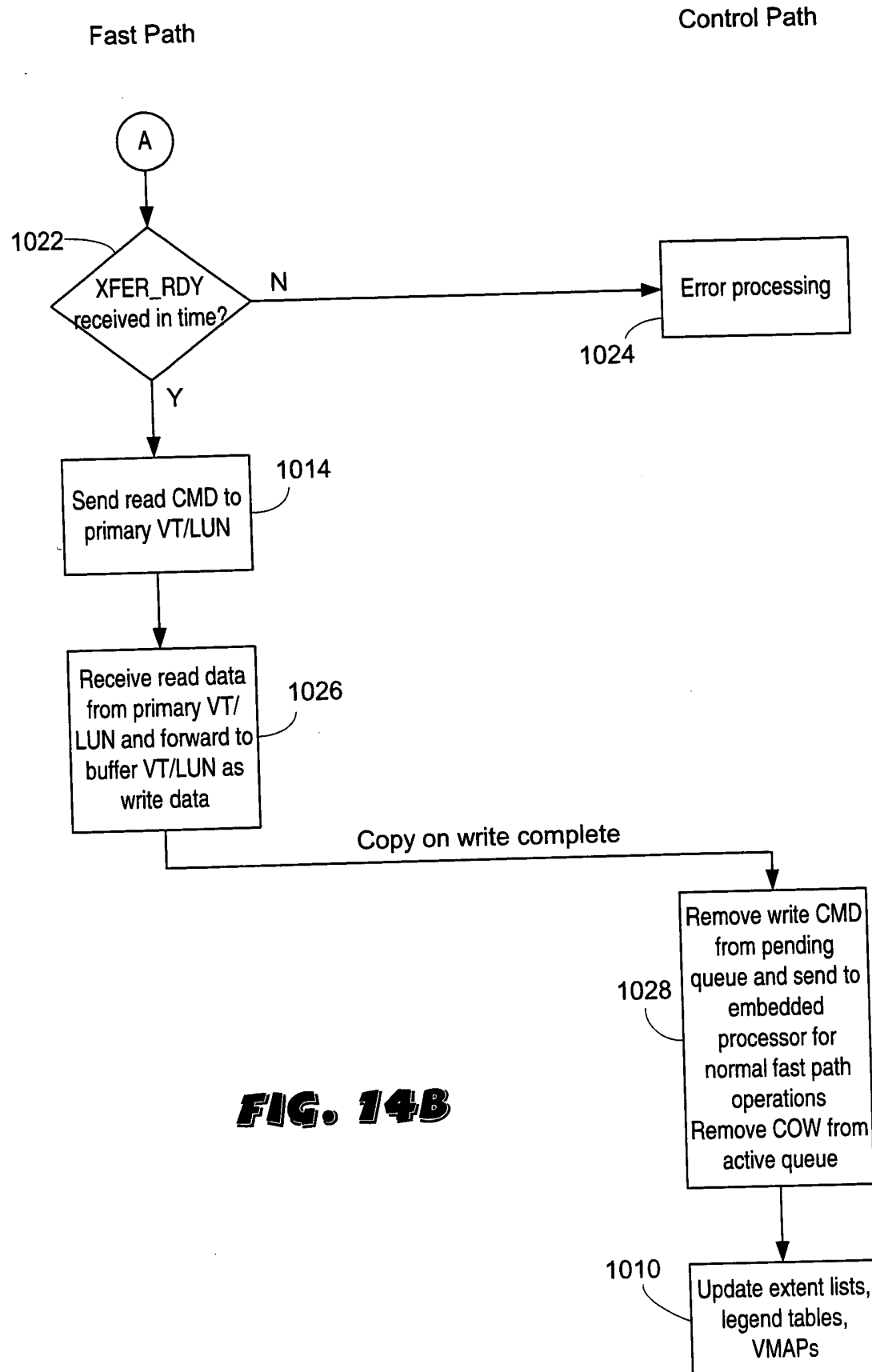


FIG. 14B

Fast Path

Control Path

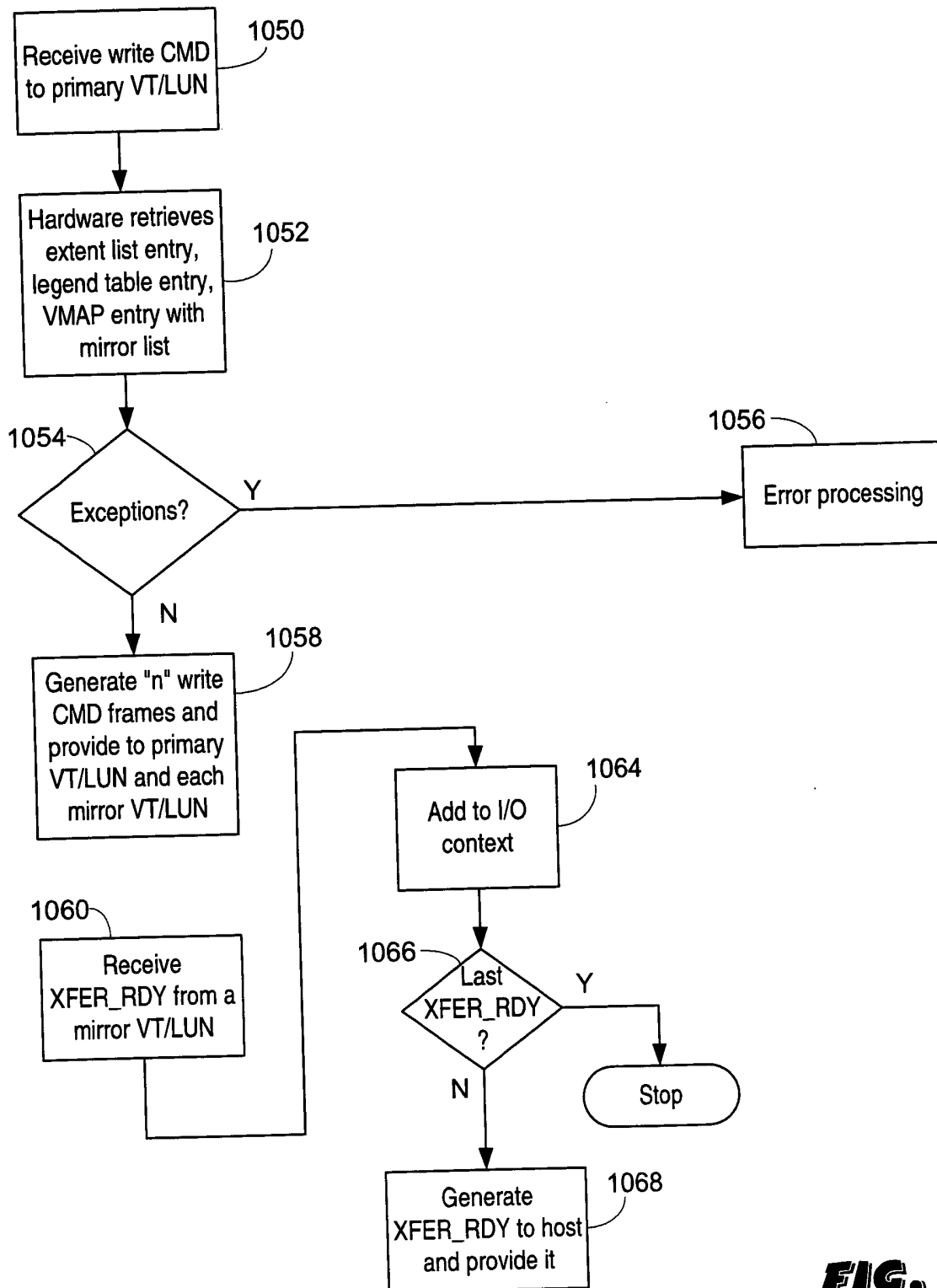


FIG. 16A

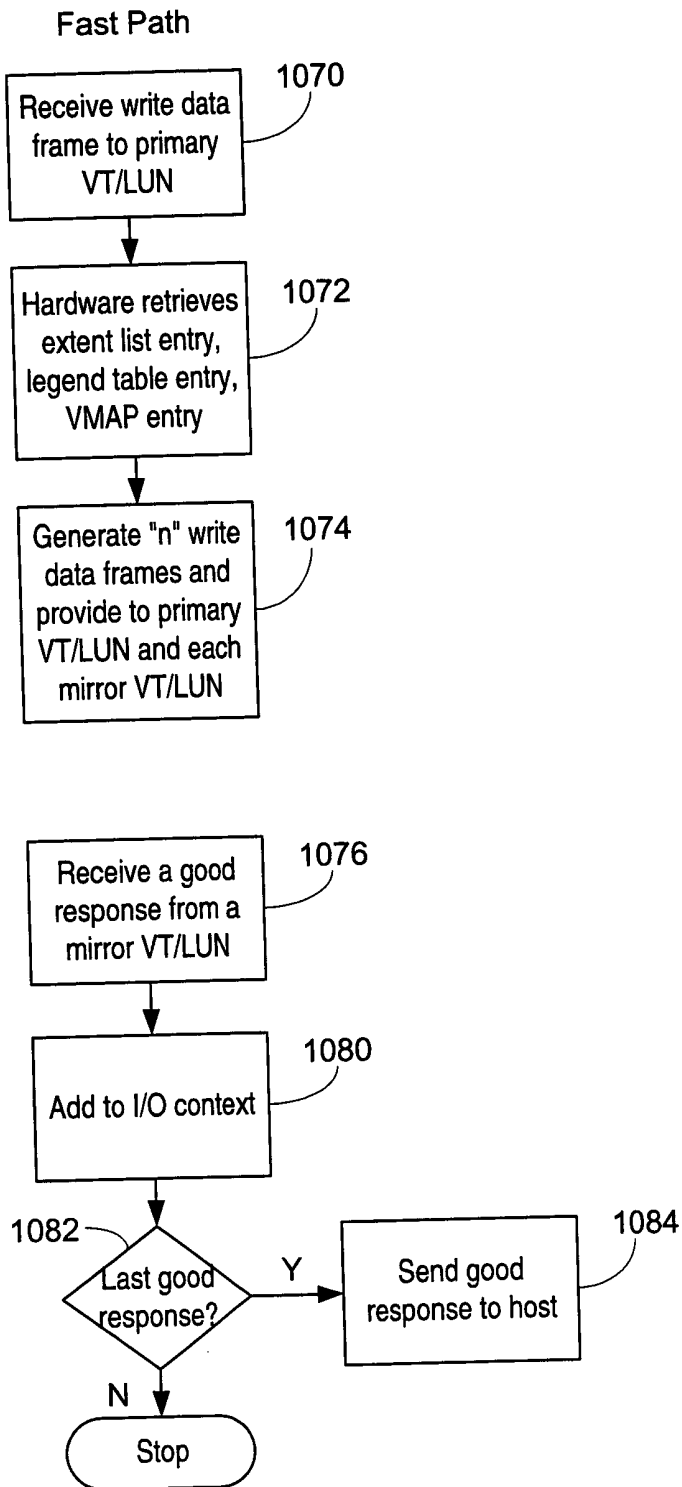


FIG. 16B

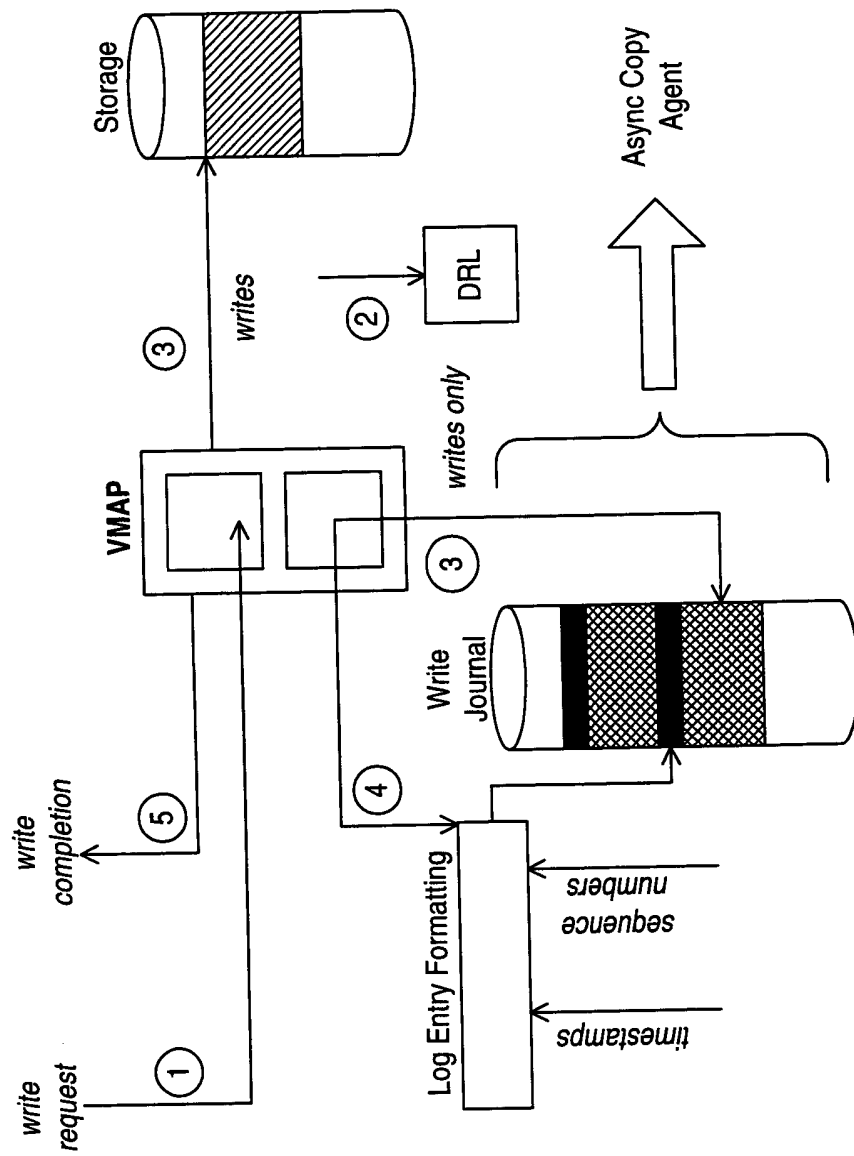
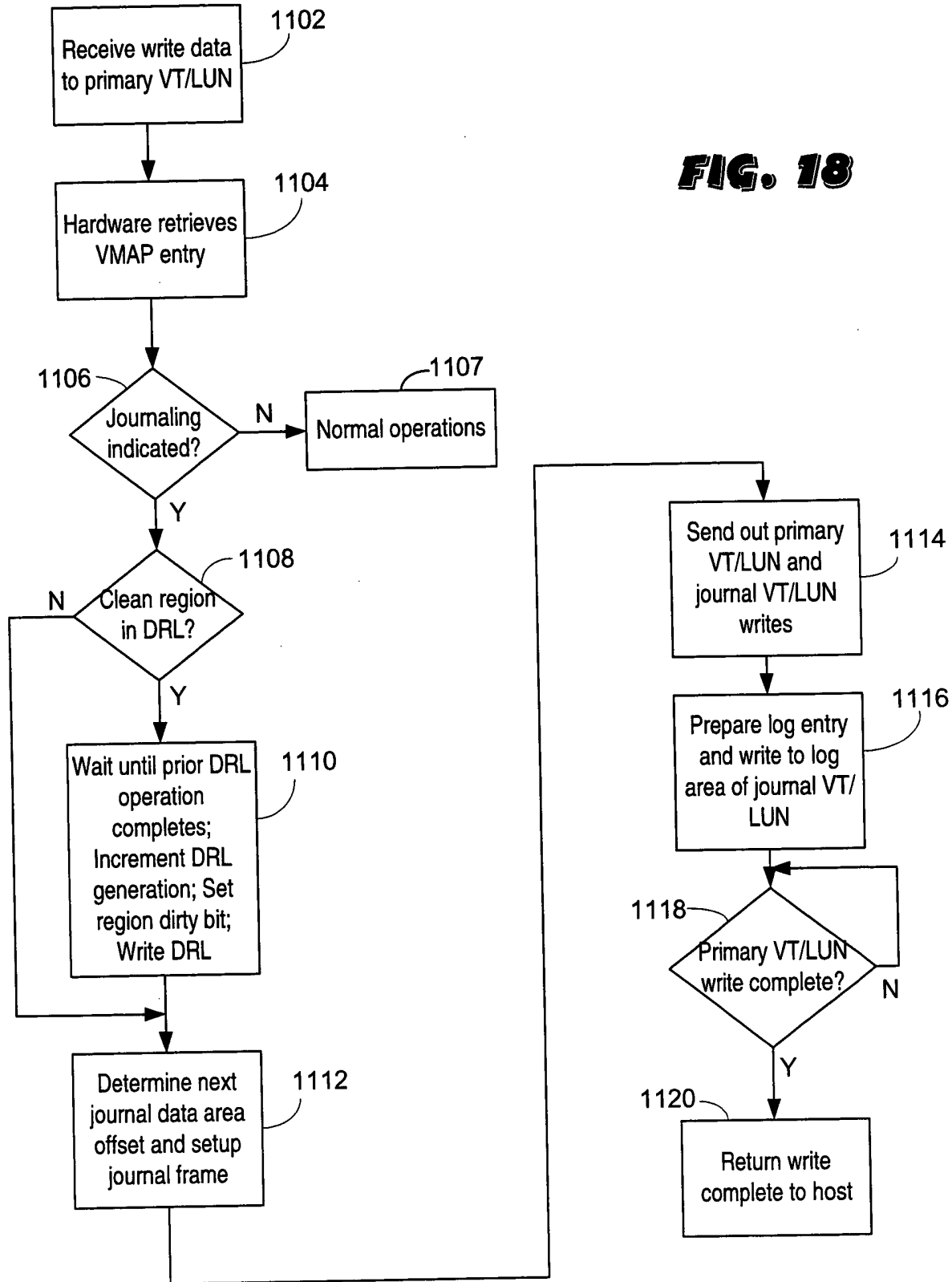


FIG. 17

Fast Path

FIG. 18



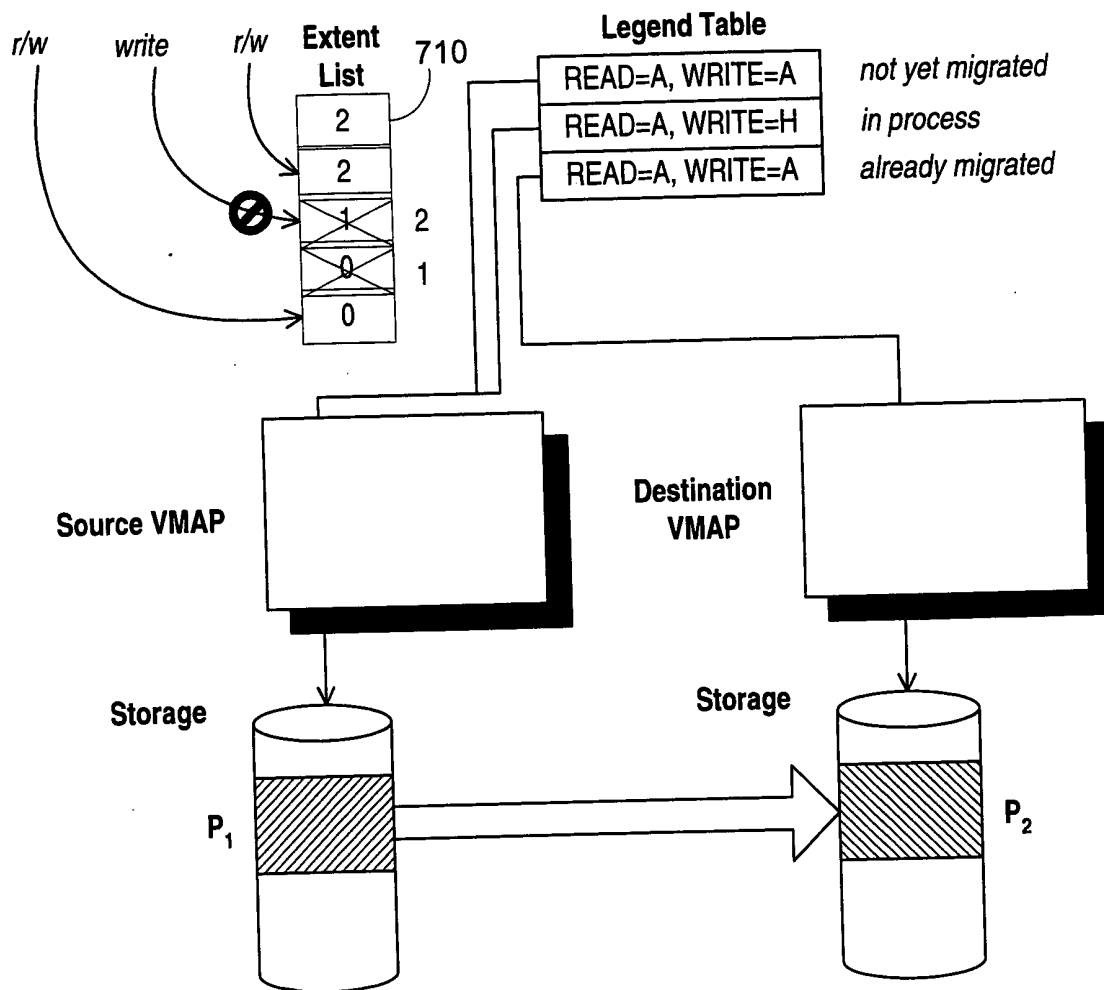


FIG. 19

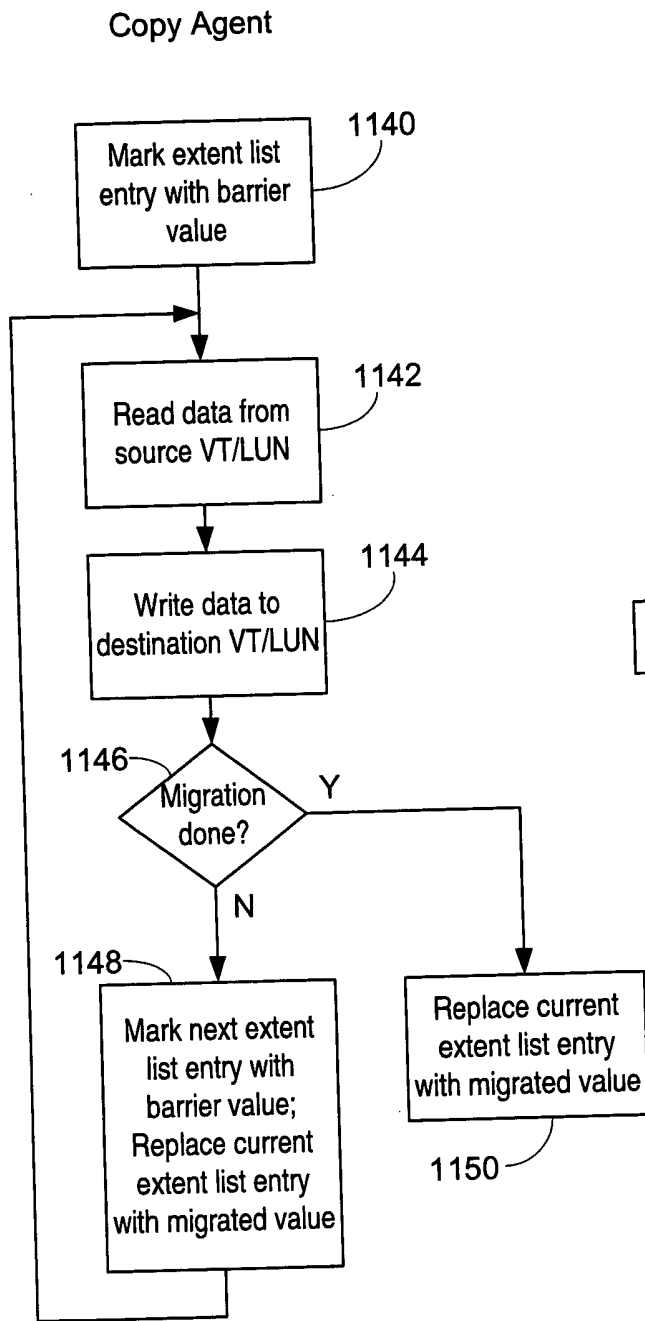


FIG. 20A

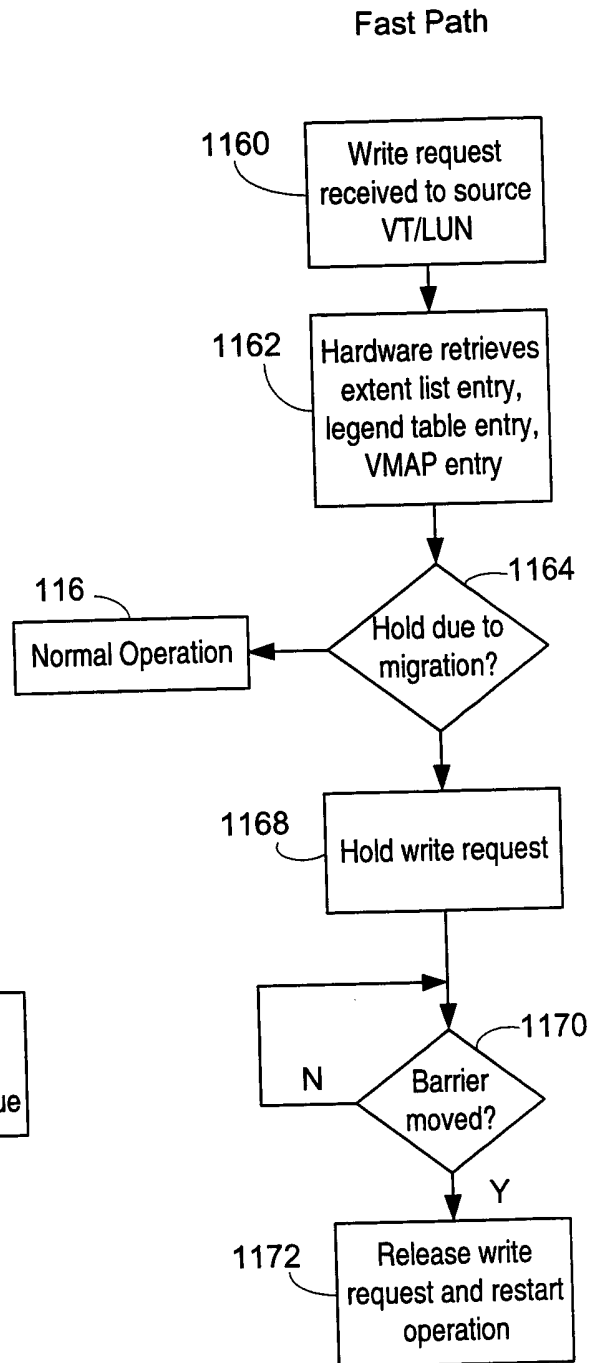


FIG. 20B

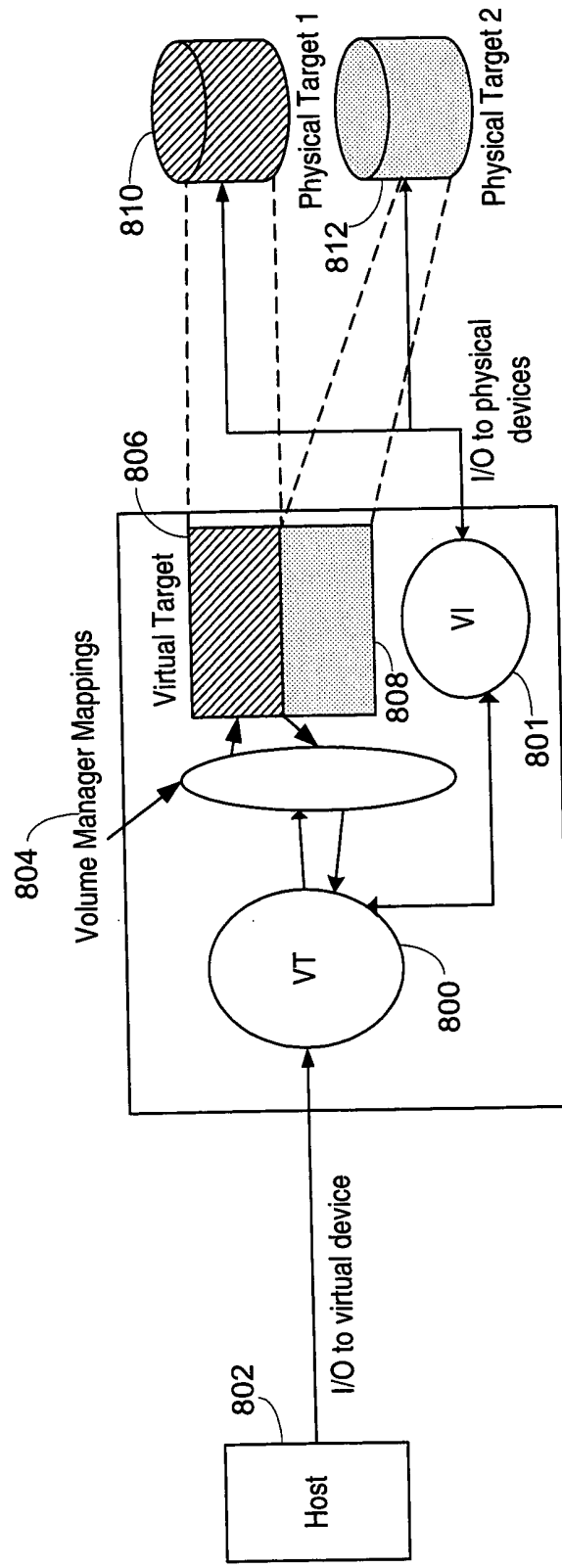


FIG. 21

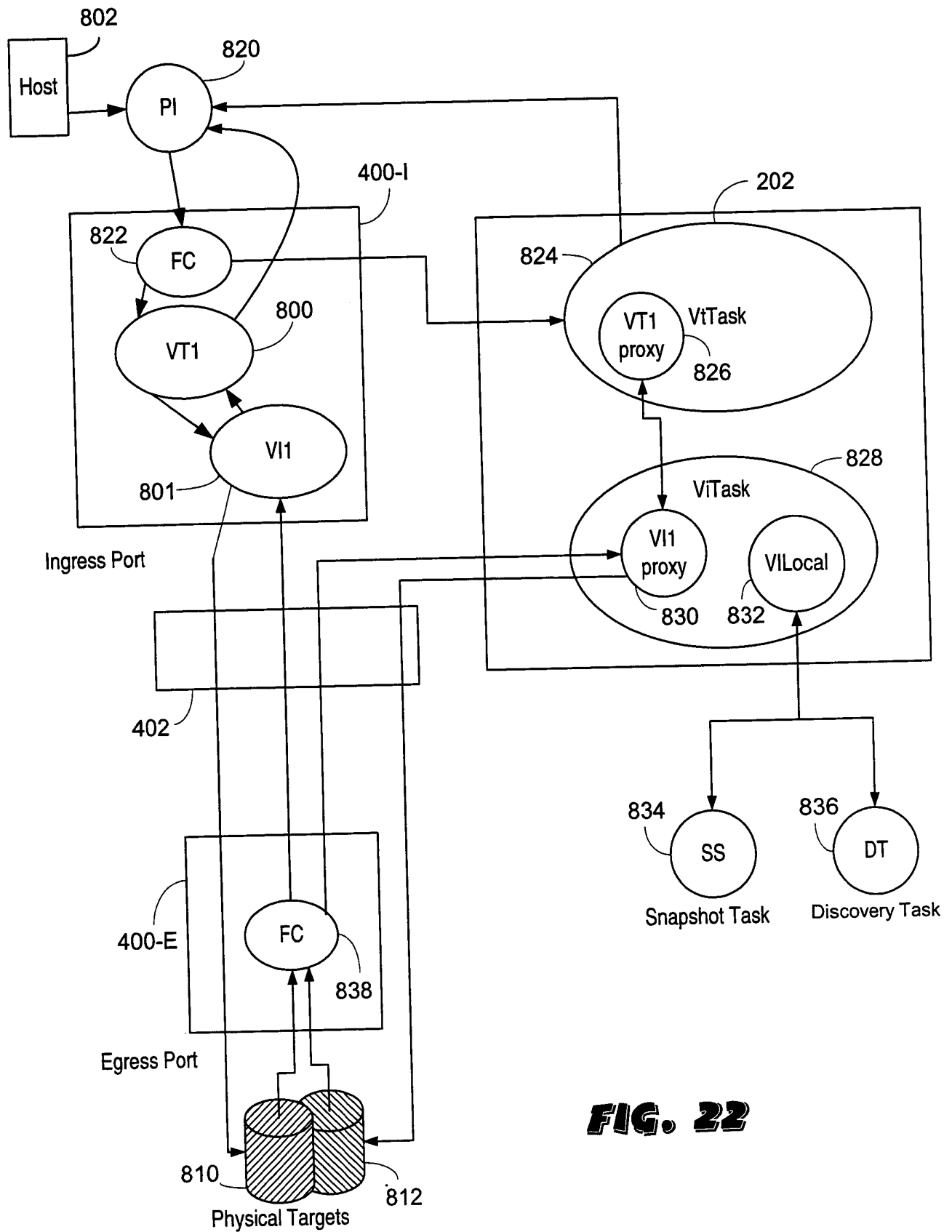


FIG. 22

Simple Read

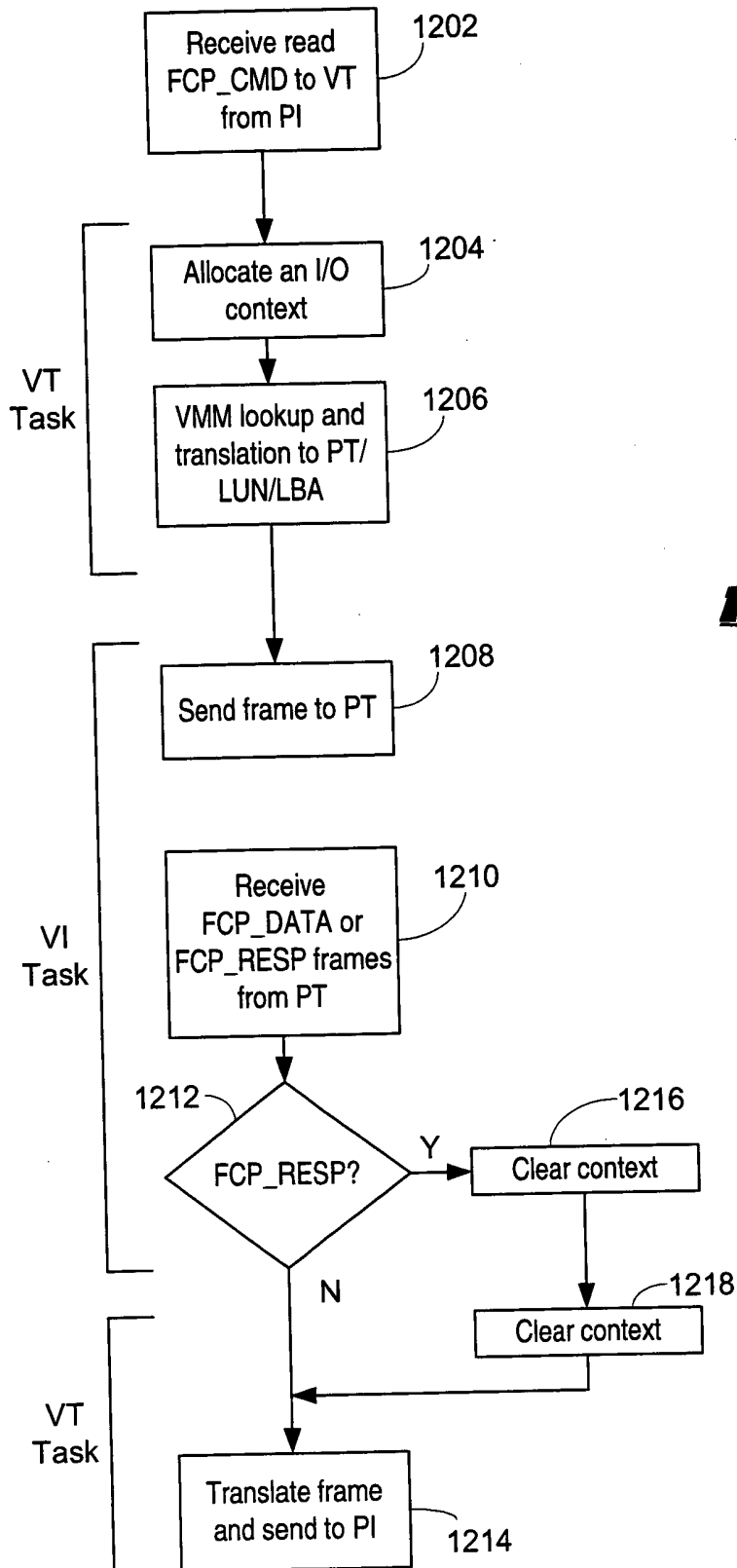


FIG. 24

Simple Write

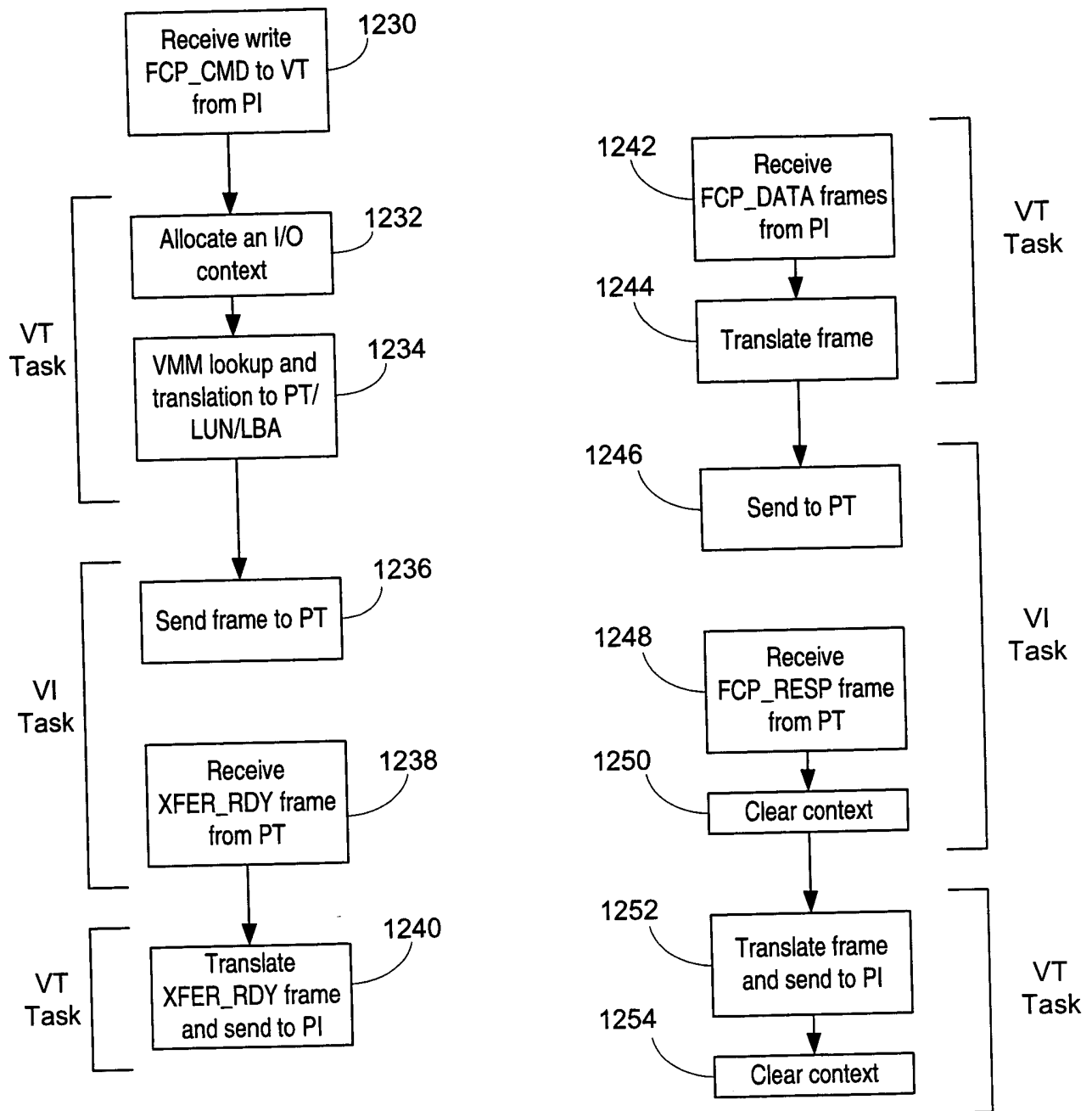


FIG. 25

Spanned Read

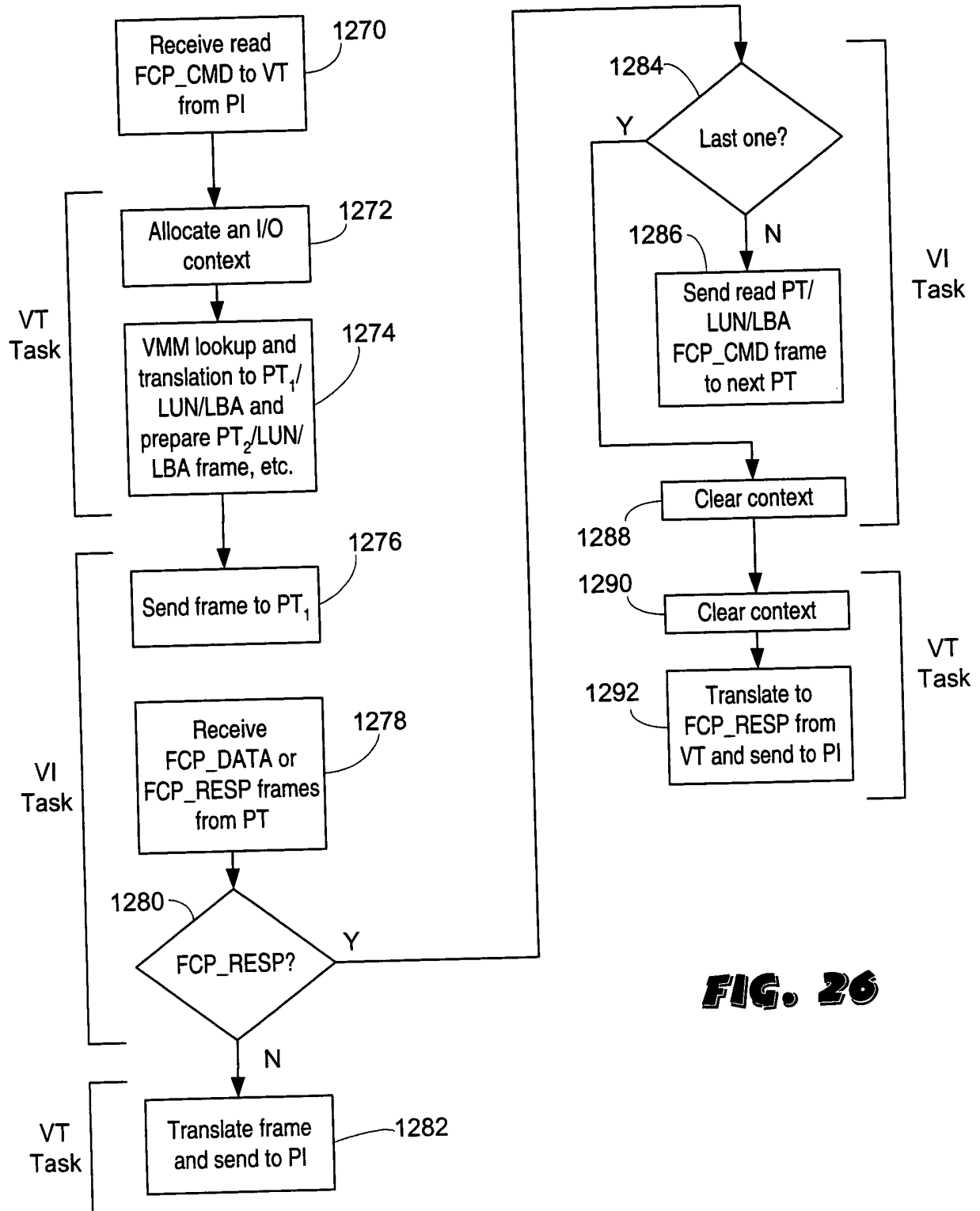


FIG. 26

